

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

Claims 1 – 10 (cancel)

Claim 11 (currently amended): A portable computer system, comprising:  
a processor;

a multi-mode wireless device on a single substrate coupled to the processor, the device comprising:

an analog portion integrated on the substrate, including:

a cellular radio core having an analog to digital converter (ADC)  
configured to receive a radio signal from an antenna, and  
a short-range wireless transceiver core; and

a digital portion integrated on the substrate, including:

a reconfigurable logic core coupled to receive converted data from the  
ADC, the reconfigurable logic core configured to handle a plurality of  
wireless communication protocols;

a plurality of general-purpose processor cores coupled to the  
reconfigurable logic core; and

a router coupled to the reconfigurable logic core, the cellular radio core,  
and the short-range wireless transceiver core to de-correlate data into  
parallel streams that are not time-correlated and to route packets of data  
via a first wireless transmission medium and a second wireless  
transmission medium; and

a memory array coupled to the reconfigurable logic core.

Claims 12 - 15 (canceled)

Claim 16 (previously presented): The portable computer system of claim 11, wherein the reconfigurable logic core includes one or more digital signal processors (DSPs).

Claim 17 (previously presented): The portable computer system of claim 11, wherein the reconfigurable logic core includes one or more reduced instruction set computer (RISC) processors.

Claims 18 ~ 20 (cancel)

Claim 21 (previously presented): The portable computer of claim 11, further comprising an input recognizer embodied in a program storage device, said input recognizer configured to receive input from a user.

Claim 22 (previously presented): The portable computer of claim 11, wherein the reconfigurable logic core comprises a vector processor.

Claim 23 (currently amended): The portable computer of claim 22, wherein the vector processor is configured to provide a parallel data output to the one or more plurality of general-purpose processor cores.

Claim 24 (previously presented): The portable computer of claim 11, wherein the digital portion comprises a reconfigurable processor core including the reconfigurable logic core and the plurality of general-purpose processor cores.